



3PLUS1 TECHNOLOGY, ACCELCHIP INC., HANTRO PRODUCTS AND SYNFORA JOIN OPEN CORE PROTOCOL INTERNATIONAL PARTNERSHIP

PORTLAND, Ore. — December 1, 2004 — Open Core Protocol International Partnership (OCP-IP) today announced that four new members are joining the organization, including: 3plus1 Technology, a fabless semiconductor company based in Saratoga, CA; AccelChip Inc., a leading provider of algorithmic synthesis and semiconductor IP for DSP designs developed in MATLAB; Hantro Products OY, a leading provider of hardware and software based MPEG4/H.263 and H.264 video solutions specifically tailored for handheld devices; and Synfora, which provides the first true “algorithm-to-tapeout” synthesis technology, enabling designers to rapidly explore and implement C algorithms in silicon. The new members represent a wide variety of markets and product offerings, illustrating the industry-wide acceptance and adoption of OCP.

OCP-IP members receive free training and support, free software tools and free documentation, enabling them to focus on the challenges of SoC design. Leveraging OCP-IP’s infrastructure eliminates the need to internally design, document, train and evolve a proprietary standard and support tools, freeing up critical resources for the real design work and providing enormous cost savings.

“3Plus1 encourages the standardization and interoperability of on chip components that OCP enables,” said Allan Cox, CEO of 3Plus1 Technology Inc. “We have long been an advocate of this initiative and have chosen OCP compliant IP as a major part of our SoC on chip interfaces.”

“Accelerating development of Embedded DSP algorithms in silicon requires a standard interface to the rest of the system,” said Vin Ratford, CEO of AccelChip Inc. “We have selected OCP-IP as our on-chip interface for ASICs and FPGAs due to its flexibility, complete specification and widespread adoption.”

“By adapting our video codecs to OCP based system designs we will strengthen our product portfolio significantly. OCP-IP helps us to extend our support to key players in the market,” explained Harri Hyvari, Vice President of Research and Development, Hantro Products.

Vinod Kathail Synfora CTO and vice-president of engineering, said “OCP-IP is member-driven in support of standards that help design engineers and their companies to produce chips and systems on time and within budget. Synfora is pleased to join the group and help develop these standards to enable application engine synthesis to accelerate and streamline SoC design.”

“We are very proud to announce and welcome all of our new members,” said Ian Mackintosh, president OCP-IP. “This new group of members represents a diverse set of commercial product offerings and further highlights OCP acceptance and adoption across the industry.”

About OCP-IP

The OCP International Partnership Association, Inc. (OCP-IP) formed in 2001, promotes and supports the Open Core Protocol (OCP) as the complete socket standard ensuring rapid creation and integration of interoperable virtual components. OCP-IP’s Governing Steering Committee participants are: Nokia [NYSE: NOK], Texas Instruments [NYSE: TXN], STMicroelectronics [NYSE: STM], Toshiba Semiconductor Group (including Toshiba America TAEC) and Sonics. OCP-IP is a non-profit corporation delivering the first fully supported, openly licensed, core-centric protocol comprehensively fulfilling system-level integration requirements. The OCP facilitates IP core reusability and reduces design time, risk and manufacturing costs for SoC designs. VSIA endorses the OCP socket, and OCP-IP is affiliated with the VSI Alliance. For additional background and membership information, visit www.OCP-IP.org.

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