



## **KAWASAKI MICROELECTRONICS (K-MICRO) JOINS OPEN CORE PROTOCOL INTERNATIONAL PARTNERSHIP (OCP-IP)**

**PORTLAND, Ore. — September 21, 2004** — Open Core Protocol International Partnership (OCP-IP), the association providing a common standard for intellectual property core interfaces, or sockets, that facilitate “plug and play” SoC design, today announced that Kawasaki Microelectronics (K-Micro) has joined the organization. Utilizing OCP technology allows K-Micro’s customers to reduce design times, risks and costs and quickly introduce highly-competitive solutions.

“OCP technology is a key element in our Matrix ASIC initiative,” said Hideki Yoneda, executive vice-president of K-Micro. “By incorporating OCP-based IP in their K-Micro Matrix ASIC, customers will lower NRE costs and speed up time-to-market for derivative ASICs. We plan to incorporate OCP technology in both our existing and new ASIC intellectual property (IP) cores, such as USB 2.0 and PCI-Express. K-Micro commends OCP-IP for driving OCP to become a widely used and adopted socket interface for SoC designs.”

OCP-IP members receive free training and support, free software tools, and free documentation, enabling members to focus on the challenges of SoC design. Leveraging OCP-IP’s powerful free infrastructure eliminates the need to design, document, train and evolve a proprietary standard and support tools, freeing up critical resources for the real design work while providing enormous cost savings.

“The SoC market is moving quickly towards a platform-based development approach, making industry collaboration essential,” said Ian Mackintosh, president of OCP-IP. “OCP IP is leading the way in driving industry collaboration and sharing this vision with the SoC design community; K-Micro shares this vision for our industry.”

### **About OCP-IP**

The OCP International Partnership Association, Inc. (OCP-IP) was announced in December 2001 to promote and support the open core protocol (OCP) as the complete socket standard that ensures rapid creation and integration of interoperable virtual components. OCP-IP's Governing Steering Committee participants are: Nokia [NYSE: NOK], Texas Instruments [NYSE: TXN], STMicroelectronics [NYSE: STM], United Microelectronics Corporation [NYSE: UMC], Toshiba Semiconductor Group (including Toshiba America TAEC), Sonics, and other industry leading companies. OCP-IP is a non-profit corporation delivering the first fully supported, openly licensed core-centric protocol that comprehensively fulfills system-level integration requirements. The OCP facilitates IP core reusability and reduces design time and risk, along with manufacturing costs for SoC designs. VSIA endorses the OCP socket, and OCP-IP is an Adoption Group of the VSI Alliance. For additional background and membership information, visit [www.ocpip.org](http://www.ocpip.org).