



ASICS WORLD SERVICES, ATRENTA, CELOXICA, FIRST SILICON SOLUTIONS, AND JEDA TECHNOLOGIES JOIN OCP INTERNATIONAL PARTNERSHIP

PORTLAND, Ore. — May 31, 2005 — Open Core Protocol International Partnership (OCP-IP) today announces that five new members are joining the organization, including: ASICS World Services, Atrenta, Celoxica, First Silicon Solutions (FS2) and Jeda Technologies. The new members represent a wide variety of markets and product offerings, illustrating the industry-wide acceptance and adoption of OCP.

ASICS World Services, LTD was founded in 1999, immediately started offering various IP Cores and design services. Since then ASICS.ws has established a world-wide reputation for professional high quality IP Cores with flexible licensing at a low cost. ASICS.ws provides a broad line of general-purpose IP cores, including a variety of USB related products, encryption (AES), error correction (Reed Solomon), and many other functions.

Headquartered in San Jose Ca, Atrenta Inc. is a provider of Predictive Development solutions for the electronic systems industry. Atrenta's ITeam Family of Predictive Development solutions enhances the entire development cycle—from architectural planning to physical implementation, and from chips to systems to embedded software.

Celoxica's suite of Electronic System Level (ESL) design solutions cover C-based design and behavioral synthesis tools, FPGA boards and programmable SoC development hardware, libraries of IP and system level APIs for co-design modeling, verification and platform abstraction. Utilized alongside partner technology for FPGA, Structured ASIC and SoC design, Celoxica's products enable digital electronics to be designed more quickly, with lower risk and at a lower cost.

Founded in 1998, First Silicon Solutions (FS2) specializes in hardware verification and debug technologies including OCI® (On-Chip Instrumentation) custom silicon IP, development tools, and design services for programming, testing and debug of embedded systems in FPGA, SoC, SOPC, ASSP and ASIC designs. FS2 provides full system solutions with IP blocks, hardware, and software tools deployed in several stages of the development cycle from system design and verification to software development and debug. FS2 products enable silicon vendors and their customers to take their designs from "first silicon" to production faster, with better understanding of the design performance and overall operation.

Jeda Technologies, Inc. is a privately held electronic design automation (EDA) company providing a hardware verification and modeling platform to verify complex FPGA, ASIC and system-on-chip (SoC) designs. Jeda delivers a comprehensive, standards-based verification and modeling solution for hardware verification and hardware/software co-design encompassing the entire design and verification cycle — including IP block, chip and system level.

OCP-IP members receive free training and support, free software tools, and free documentation, enabling them to focus on the challenges of SoC design. Leveraging OCP-IP's infrastructure eliminates the need to internally design, document, train and evolve a proprietary standard and support tools, freeing up critical resources for the real design work and providing enormous cost savings.

“ASICS World Services, LTD is very pleased to join the OCP-IP alliance,” said Rudolf Usselman, General Manager of ASICS.ws. “Immediately after joining OCP-IP we started offering our most popular IP Cores ready for integration in to OCP based systems. These IP Cores include the very popular USB 2.0 OTG IP Core, USB 2.0 Device IP Core, SD/SDIO/MMC Host IP Core, Serial ATA I/II IP core, and numerous other IP cores.”

According to Atrenta's chief technology officer, Bernard Murphy, “Platform-based design is a rapidly growing trend as a means of achieving significantly higher turnaround times in system development. This trend aligns naturally with Atrenta's predictive development solutions that enable fast turnaround times by turning the system development flow into a predictable, precise and manageable process. Atrenta sees OCP-IP as a leading enabler for platform-based design and we look forward to working together to advance this common goal.”

"With unrelenting pressures on design turnaround, productivity and product differentiation there is clear value in an industry standard socket that better supports the integration of custom IP with existing core reuse," said Jeff Jussel, Celoxica vice president of marketing. "OCP-IP provides open plug-and-play design effort that can integrate with ESL design."

"First Silicon Solutions is pleased to announce its membership in OCP-IP, in conjunction with development of our OCP Navigator bus level debug products. OCP is an important component in increasingly sophisticated systems in silicon and we look forward to working with our OCP partners to provide IP and tools to facilitate the development and debug of OCP based products." said Rick Leatherman, President of First Silicon Solutions.

"Jeda is committed to supporting standards that enable us to address the challenges of SOC design and verification" stated Glen Jones, Director of Marketing at Jeda Technologies. "We see OCP as a key element of the virtual component design paradigm, and will assist customers that can benefit from its connectivity capabilities".

"This new group of members represents a diverse set of commercial product offerings, and further highlights the broad OCP acceptance and adoption across the industry," said Ian Mackintosh, president OCP-IP. "We are very proud to announce and welcome all of our new members, and look forward to working with them in the future."

About OCP-IP

The OCP International Partnership Association, Inc. (OCP-IP), formed in 2001, promotes and supports the Open Core Protocol (OCP) as the complete socket standard ensuring rapid creation and integration of interoperable virtual components. OCP-IP's Governing Steering Committee participants are: Nokia [NYSE: NOK], Texas Instruments [NYSE: TXN], STMicroelectronics [NYSE: STM], Toshiba Semiconductor Group (including Toshiba America TAEC), and Sonics. OCP-IP is a non-profit corporation delivering the first fully supported, openly licensed, core-centric protocol comprehensively fulfilling system-level integration requirements. The OCP facilitates IP core reusability and reduces design time, risk, and manufacturing costs for SoC designs. VSIA endorses the OCP socket, and OCP-IP is affiliated with the VSI Alliance. For additional background and membership information, visit www.OCP-IP.org.

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