



Nascentric Joins OCP-IP

PORTLAND, Ore. — March 3, 2005 — Open Core Protocol International Partnership (OCP-IP) today announces that Nascentric, Inc. is joining the organization. Nascentric, headquartered in Austin, Texas is developing state-of-the-art simulation and analysis solutions to address the timing, power and signal integrity issues associated with today's complex SoC designs.

In early 2005, Nascentric will announce a high-performance, SPICE-accurate simulator, as well as other analysis tools, that allow design teams to rapidly analyze and optimize their design while considering the interdependent nature of timing, power and signal integrity. Leveraging its 10x advantage over other fast-SPICE simulators, Nascentric's technology will allow design teams to address larger, more complex circuits faster than previously possible. The combination of high-performance and SPICE-level accuracy will help design teams reduce iterations and improve the quality of the design, resulting in higher yields.

Successful development and implementation of IP is integral to meeting a design's performance and functional objectives. In addition to needing strong underlying integration technology, effective implementation of IP blocks requires that boundary conditions be well understood and fully characterized. Recently, IP blocks have reached the size that full chips were a few years ago. Integration of these large IP blocks requires a full chip simulation to insure proper timing, power and signal integrity at the integration boundaries of the IP.

"We're pleased to be accepted as members of the OCP-IP organization," said Vess Johnson, president and CEO of Nascentric, Inc. "Providing aids and training in IP development and reuse benefits IP vendors, design teams and the entire EDA community. Adoption of the OCP-IP standards and the use of well characterized boundary conditions will go a long way toward eliminating costly delays in the design cycle."

OCP-IP members receive free training, support, software tools, and documentation. This infrastructure allows IP and EDA vendors to eliminate the need to internally design, document, train and evolve a proprietary standard and set of support tools. This enables IP and EDA vendors to focus their efforts and resources on the challenges of developing IP that can be quickly integrated and easily verified in a wide variety of SoC designs. As a result, IC design teams can dedicate their critical resources to the design and delivery of products.

"Nascentric's innovative current-based simulation and analysis technology is truly unique in the industry," said Ian Mackintosh, president of OCP-IP. "Verifying boundary conditions has been a time consuming, but necessary part of delivering high-quality IP that can be quickly integrated into a design. For this reason we're delighted to see Nascentric adopt and endorse OCP."

About Nascentric

Nascentric is a privately held company headquartered in Austin, Texas. Nascentric delivers state-of-the-art analysis solutions addressing nanometer design issues related to timing, power and signal integrity that enable designers to quickly analyze larger and more complex circuits, improve design quality and facilitate higher yields. For more information, visit the website at www.nascentric.com.

About OCP-IP

The OCP International Partnership Association, Inc. (OCP-IP), formed in 2001, promotes and supports the Open Core Protocol (OCP) as the complete socket standard ensuring rapid creation and integration of interoperable virtual components. OCP-IP's Governing Steering Committee participants are: Nokia [NYSE: NOK], Texas Instruments [NYSE: TXN], STMicroelectronics [NYSE: STM], Toshiba Semiconductor Group (including Toshiba America TAEC), and Sonics. OCP-IP is a non-profit corporation delivering the first fully supported, openly licensed, core-centric protocol comprehensively fulfilling system-level integration requirements. The OCP facilitates IP core reusability and reduces design time, risk, and manufacturing costs for SoC designs. VSIA endorses the OCP socket, and OCP-IP is affiliated with the VSI Alliance. For additional background and membership information, visit www.OCPIP.org.

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