



COWARE AND MIPS TECHNOLOGIES WIN OCP-IP OUTSTANDING CONTRIBUTOR OF THE YEAR AWARDS FOR 2005

PORTLAND, Ore. — September 21, 2005 — Open Core Protocol International Partnership (OCP-IP), today announced that CoWare and MIPS Technologies, Inc. are joint winners of the Outstanding Contributor of the Year Award for 2005. The Award is given each year by the OCP-IP Governing Steering Committee to a member or members who have made key contributions to the further advancement of the OCP specification or surrounding infrastructure.

CoWare was acknowledged for contributions in the OCP-IP System Level Design Working Group. The Company made key contributions to the development of the SystemC Transaction Level Modeling (TLM) Channel version 2.1.1 and the methodology white paper, "OCP TLM for Architectural Modeling." The new features in version 2.1.1 improve model interoperability, resulting in better productivity in system level modeling.

The white paper is an influential methodology contribution to transaction level modeling. It outlines the use of the OCP TLM in the same flow with the OSCI TLM, and introduces a newly defined modeling use case, Architects View, using OCP TL2 or TL3 abstraction level for interface modeling. The white paper is accompanied with a package of SystemC code that has extensive examples for modeling in different abstraction levels, and for making models of different abstractions interoperable.

"The new transactional channel brings us even greater system-level model interoperability and reusability as a part of the OCP socket; CoWare's contribution was key to the development of the transactional models," said Anssi Haverinen, senior specialist at Nokia Technology Platforms. "It was a pleasure leading the collaboration between such productive members of OCP-IP's System-Level Design Working Group."

"We see OCP-IP's work gaining momentum with our customers, driven by the need for interoperability based on a truly open communications protocol," said A.K. Kalekos, vice president of marketing and business development at CoWare. "The leading-edge work of OCP's System-Level Design Working Group is complimented by OSCI's work on TLM, and we were happy to contribute our expertise to keep them in sync. We're delighted to be recognized for our efforts, with this award from OCP-IP."

MIPS Technologies is recognized for its work in the OCP-IP Specification Working Group, in particular for its contributions in the area of out-of-order tagging. Tagging provides the ability for interconnect and targets to re-order transactions for non-conflicting memory addresses within a single thread. Unlike threads, which enforce no ordering restrictions, tagged transactions ensure that read/write hazards are respected by the system. Tagged transactions are particularly attractive for advanced embedded CPU architectures, like the MIPS32[®] 24K[®] processor core family, which can exploit the parallelism offered by out-of-order transaction processing, but require consistent memory ordering.

"We appreciate OCP-IP's acknowledgement of our contributions to the industry. Our collaboration has enabled designers to more easily bring complex SoC designs to market quickly and efficiently," said Russ Bell, vice president of marketing at MIPS Technologies. "MIPS Technologies looks forward to future innovation with OCP-IP for improving standard interfaces and buses for the embedded market."

"OCP has a robust, thriving infrastructure driven by many independent companies providing leading-edge services and products," said Ian Mackintosh, president of OCP-IP. "We are proud of the tremendous work completed by the joint winners CoWare and MIPS, and are pleased to present them with the Outstanding Contributor Awards for 2005. We look forward to continued collaboration in the future."

For more information please visit www.ocpip.org

About OCP-IP

The OCP International Partnership Association, Inc. (OCP-IP), formed in 2001, promotes and supports the Open Core Protocol (OCP) as the complete socket standard ensuring rapid creation and integration of interoperable virtual components. OCP-IP's Governing Steering Committee participants are: Nokia [NYSE: NOK], Texas Instruments [NYSE: TXN], STMicroelectronics [NYSE: STM], Toshiba Semiconductor Group (including Toshiba America TAEC), and Sonics. OCP-IP is a non-profit corporation delivering the first fully supported, openly licensed, core-centric protocol comprehensively fulfilling system-level integration requirements. The OCP facilitates IP core reusability and reduces design time, risk, and manufacturing costs for SoC designs. VSIA endorses the OCP socket, and OCP-IP is affiliated with the VSI Alliance. For additional background and membership information, visit www.OCP-IP.org.

*OCP-IP Association, Inc.
5440 SW Westgate Drive, Suite 217, Portland, Oregon 97221 USA
Tel: 1-503-291-2560 Fax: 1-503-297-1090
E-mail: admin@ocpip.org
www.ocpip.org*