



## **OCP-IP Announces Five New Members: Averant, HDL Dynamics, KETI, PLS Development Tools and Silicon Labs**

**BEAVERTON, OR — September 12, 2007** — Open Core Protocol International Partnership (OCP-IP) today announced five new members: Averant Inc., HDL Dynamics, Korea Electronics Technology Institute (KETI), PLS Development Tools, and Silicon Laboratories Inc. The new members represent a breadth of technologies, thus illustrating a combined industry-wide acceptance and adoption of the OCP standard.

Averant Inc., founded in 1997, is a privately held EDA firm pioneering new methodologies and technologies for static formal verification. Averant's flagship product Solidify™ is a robust formal verification engine that provides the basis for property-based design and protocol verification, as well as, automatic design checks. These tools are easily adopted into the design flow and help improve quality, reduce risk, and accelerate the design process.

Founded in 2000 and headquartered in Texas, HDL Dynamics is focused on solutions for emerging areas to SoC and multicore infrastructure and development. They offer a range of digital systems IP, including instrumentation blocks and customized cores, as well as bus interfaces. HDL Dynamics provides SoC and IP development and consulting services for RTL and ESL Design (including VHDL Verilog, and SystemC modeling and tooling), Design for Debug (OCP custom On-Silicon Instrumentation) and related areas of analysis and integration between instrumentation and EDA environments.

KETI, established in 1991, is a specialized research institute in the electronics and information fields. The company contributes to the reinforcement of international competitiveness in the electronics, information and other related components industries, by encouraging the R&D necessary for technological innovation and support of small and medium-sized enterprises. KETI develops specialized areas and works to improve specialized research that fosters not only special research on technology for venture, small and medium enterprises, but also R&D projects at the front of key technologies.

PLS Development Tools is one of the leading development tools manufacturers of debug and emulators. These tools are specialized for the 16bit and 32bit microcontroller architectures.

Silicon Laboratories Inc. is a leader in the innovation of high-performance, analog-intensive, and mixed-signal ICs. Headquartered in Austin, Texas, Silicon Labs is a global enterprise with worldwide operations, sales and design activities. Silicon Labs' application-specific and general purpose products serve a broad set of markets and applications, including consumer, communications, computing, industrial and automotive. Silicon Labs is a fabless semiconductor company. For more information, please visit [www.silabs.com](http://www.silabs.com).

OCP-IP members receive free training and support, software tools, and documentation, enabling them to focus on the challenges of SoC design. Leveraging OCP-IP's infrastructure eliminates the need to internally design, document, train and evolve a proprietary standard and accompanying support tools, freeing up critical resources for the real design work, while providing enormous cost savings.

*OCP-IP Association, Inc.*  
3855 SW 153<sup>rd</sup> Drive, Beaverton, Oregon 97006 USA  
Tel: 1-503-619-0560 Fax: 1-503-644-6708 E-mail: [admin@ocpip.org](mailto:admin@ocpip.org)  
[www.ocpip.org](http://www.ocpip.org)

“This new group of members represents a diverse set of product offerings, and highlights the broad OCP acceptance and adoption across many markets,” said Ian Mackintosh, president of OCP-IP. “We are very proud to announce and welcome our new members, and look forward to working with them in the future.”

### **About OCP-IP**

The OCP International Partnership Association, Inc. (OCP-IP), formed in 2001, promotes and supports the Open Core Protocol (OCP) as the complete socket standard ensuring rapid creation and integration of interoperable virtual components. OCP-IP's Governing Steering Committee participants include: Nokia [NYSE: NOK], Texas Instruments [NYSE: TXN], Toshiba Semiconductor Group (including Toshiba America TAEC) and Sonics Inc. OCP-IP is a non-profit corporation delivering the first fully supported, openly licensed, core-centric protocol which comprehensively fulfills system-level integration requirements. The OCP facilitates IP core reusability and reduces design time, risk, and manufacturing costs for SoC designs. For additional background and membership information, visit [www.OCPIP.org](http://www.OCPIP.org).

For additional information, please contact:

Ian Mackintosh, OCP-IP  
408-761-5980  
[ian@ocpip.org](mailto:ian@ocpip.org)

Joe Basques, VitalCom  
512-249-6264  
[joe@vitalcompr.com](mailto:joe@vitalcompr.com)

NOTE: All trademarks and service marks are the property of their respective owners.