



OCP-IP AND NEXUS ANNOUNCE COLLABORATION ON INDUSTRY STANDARD DEBUG EFFORTS

BEAVERTON, ORE. — August 28, 2007 — OCP-IP and the NEXUS 5001™ Forum jointly announced a collaboration agreement that will allow more rapid creation of debug solutions. Collaboration will ensure an improved and more integrated on-chip debug tools solution for SoC, SW and IP. By working together, Nexus and OCP-IP will enable their respective members to develop aligned and more unified end-to-end debug solutions.

OCP-IP sponsors an active Debug Working Group focused on problems and approaches related to improving standards-based multi-core debug. The Debug Working Group has recently announced the availability of a white paper discussing a standardized Debug Interface Socket and OCP-bus compliant debug interfaces for IP blocks and cores to support on-chip core, multicore, and systems analysis and debug needs for software, hardware, and mixed SoC prototyping. The OCP-IP Debug strategy will be compatible with complementary industry standards efforts, with initial focus on JTAG (IEEE1149.1) and Nexus (IEEE-ISTO 5001™) interfaces.

“Nexus members include many of the leading companies in the areas of embedded systems debug, many of whom are also members of OCP-IP,” said Ian Mackintosh, President of OCP-IP. “By collaborating with the Nexus 5001™ Forum, we ensure that the work of both groups remains compatible and complimentary in progressing to provide cohesive and complete, end-to-end debug solutions.”

The Nexus 5001™ Forum was created in 1998 and is focused upon on-chip debug solutions based on the IEEE-ISTO 5001™, The Nexus 5001™ Standard for a Global Embedded Processor Debug Interface. Nexus based solutions provide a standard for high performance real-time multi-core debug, trace, calibration, rapid prototyping and on-chip analysis that has been proven in chip designs by several member companies on multiple architectures.

“By coordinating the Nexus and OCP-IP debug activities, customers will see improved core debug interfaces and new debug features that leverage the OCP-IP Debug Interface Socket and Nexus’ high performance interface standards” said Dr. Neal Stollon, President of HDL Dynamics, which has been involved with both groups and is developing debug solutions based on both the OCP-IP Debug Interface Socket and the Nexus interface.

To download a copy of the OCP-IP debug white paper, visit
http://www.ocpip.org/socket/whitepapers/OCP-IP_Debug_Working_Group_Whitepaper_3_26_2007.pdf

To download a copy of the Nexus white paper and specification, visit
<http://www.nexus5001.org>

About OCP-IP

The OCP International Partnership Association, Inc. (OCP-IP), formed in 2001, promotes and supports the Open Core Protocol (OCP) as the complete socket standard ensuring rapid creation and integration of interoperable virtual components. OCP-IP's Governing Steering Committee participants include: Nokia [NYSE: NOK], Texas Instruments [NYSE: TXN], Toshiba Semiconductor Group (including Toshiba America TAEC), and Sonics. OCP-IP is a non-profit corporation delivering the first fully supported, openly licensed, core-centric protocol comprehensively fulfilling system-level integration requirements. The OCP facilitates IP core reusability and reduces design time, risk, and manufacturing costs for SoC designs. For additional background and membership information, visit www.OCP-IP.org.

About Nexus 5001™ Forum

The Nexus 5001™ Forum was formed in 1998 and its membership spans the semiconductor, development tools and automotive electronics industries. Although the Forum initially focused on the stringent requirements of microprocessor debug tools for automotive powertrain applications, its overall goal is to enable the improved development of state-of-the-art, easy-to-use, high-performance processor and SoC analysis architectures and tools. The Nexus 5001™ Forum is a program of the IEEE Industry Standards and Technology Organization (IEEE-ISTO). For additional background and membership information, visit <http://www.nexus5001.org>.

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