

CatsEye

A MIPS24K Base Platform

For SoC Designs

Kawasaki Microelectronics America
Rev. 1.4

Is a Platform ASIC right for you?

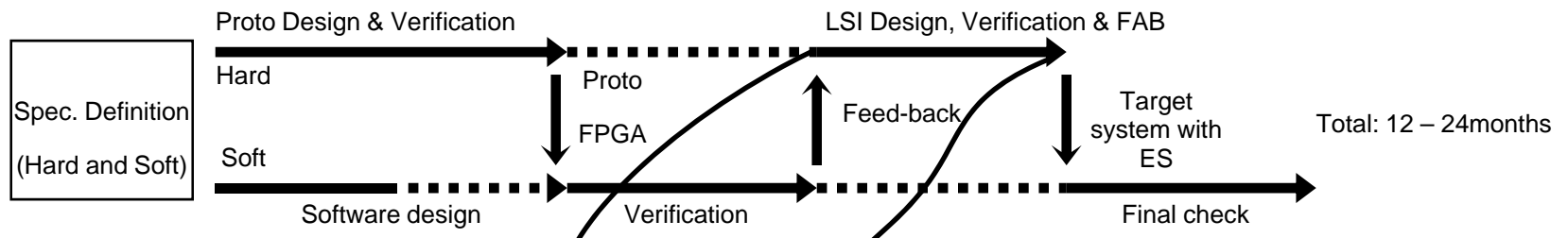
Do you want

- Faster time to market
- Lower development cost
- Lower risk
- Designs done "In-House" with fewer tools
- Concurrent hardware and software development

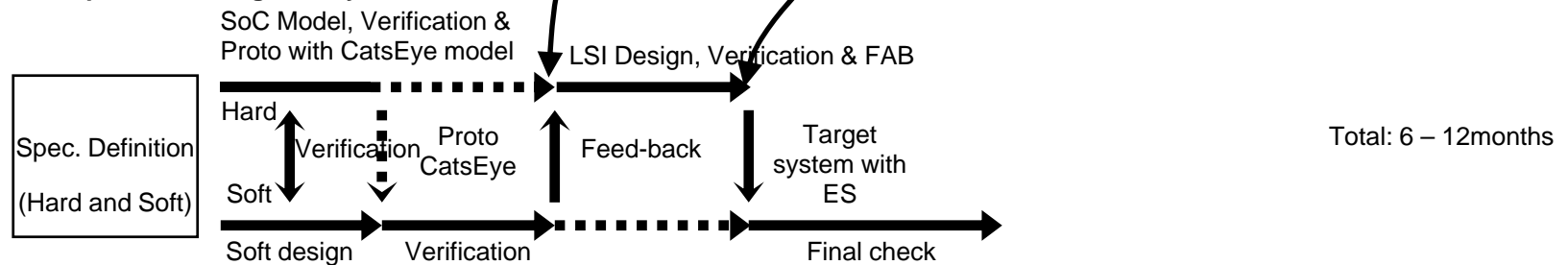
CatsEye SoC ASIC Development

Save 6 to 12 months

Current style of SoC ASIC development



SoC Development utilizing CatsEye



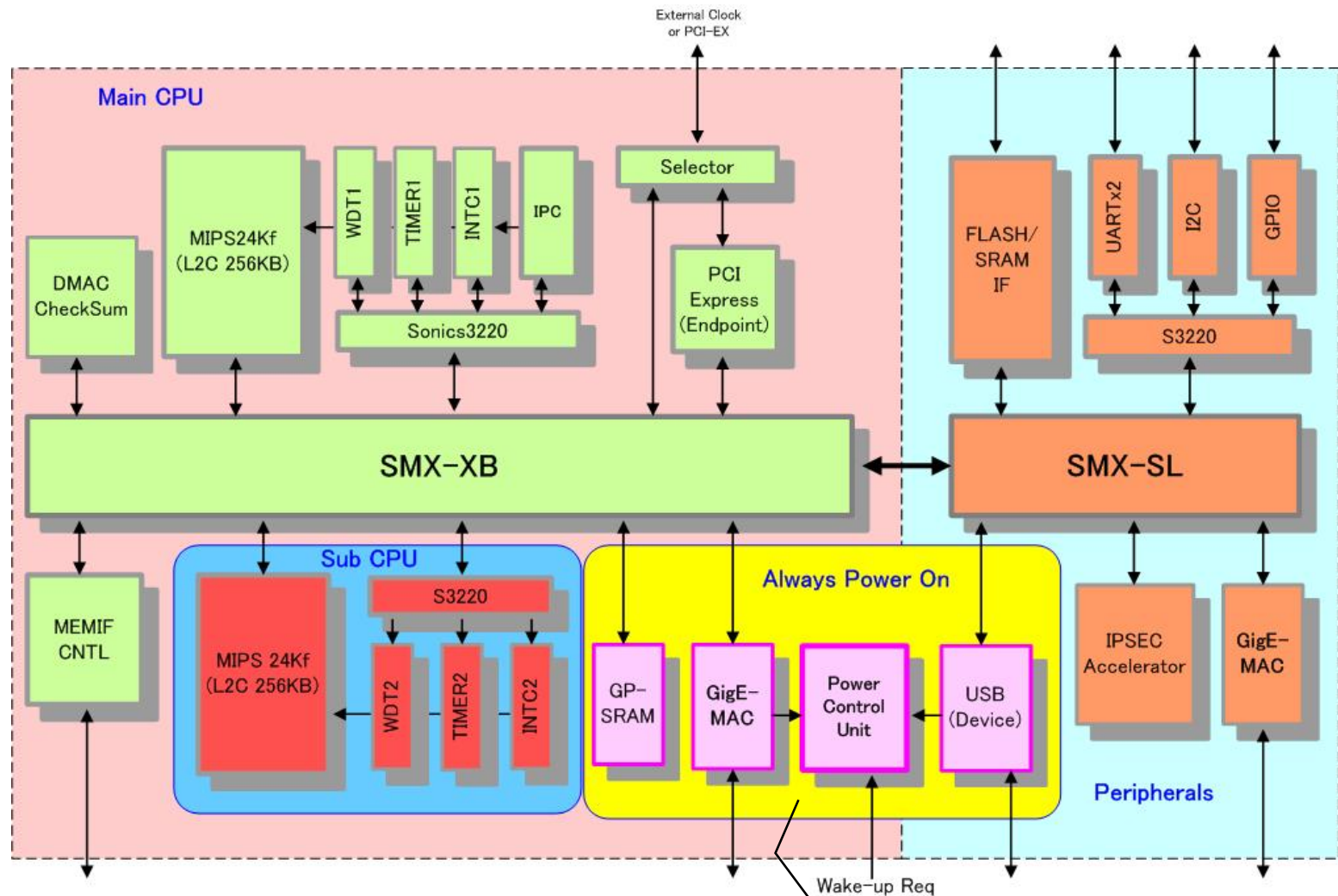
How Does it Work?

- The starting point
 - Working SoC and modify it for your application
 - Working Linux or uTRON drivers and firmware
- Easily add and remove functions
 - Plug-in boards enable adding functionality to the design
 - Develop firmware and software during the development
 - Select from our large portfolio of verified building blocks or
 - Reuse or develop your own building blocks
- Use the tools that have already been developed as the starting point
 - Sonics Studio makes changing the design simple and easy
 - It is easy to incorporate your IP into the design with the SMX bus and tools set

Development Support

- K-micro provides support during the development
- Tool support
 - Tools
 - Functions
 - Software

The Platform Starting Point

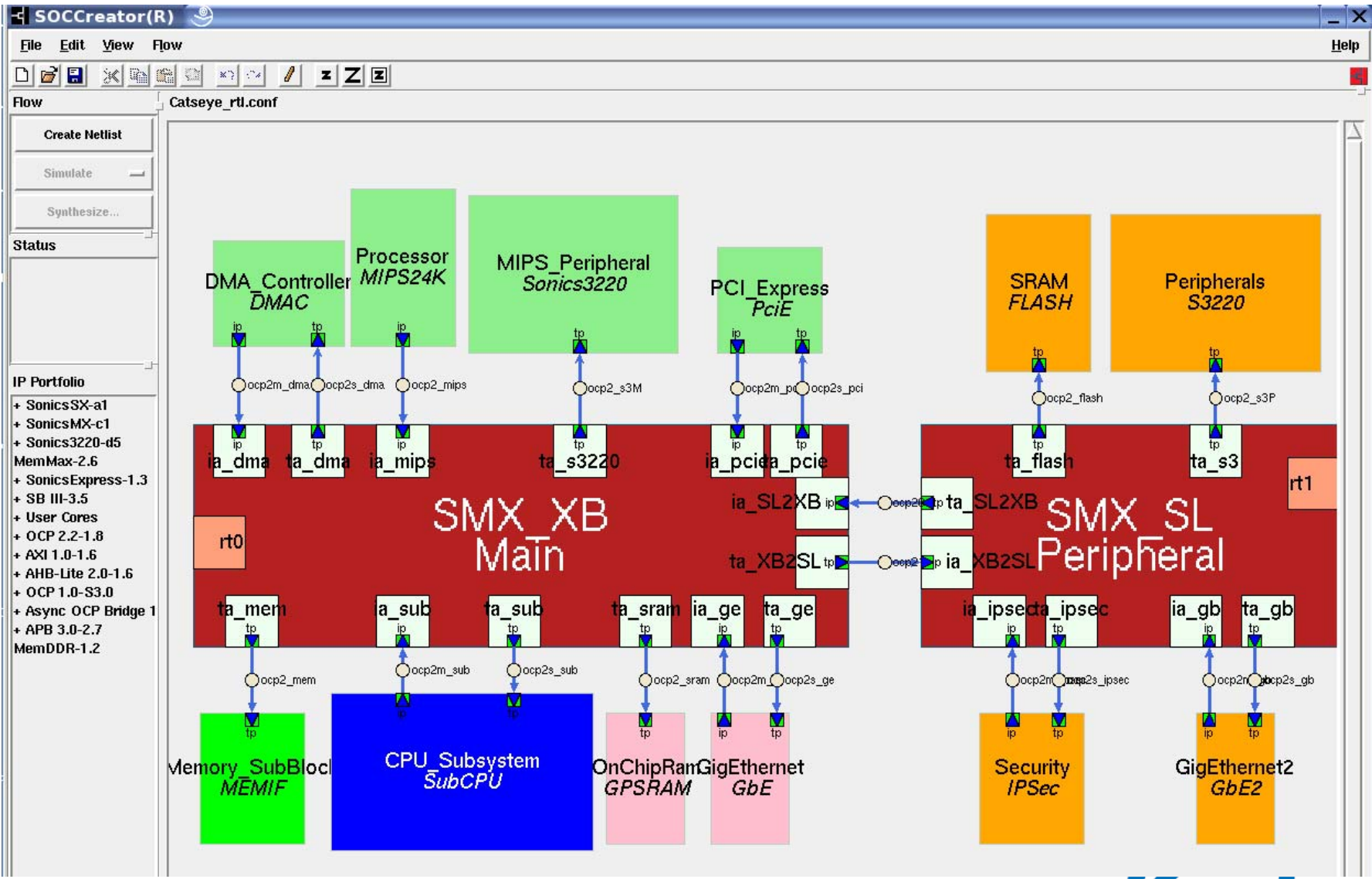


Wake-On-LAN/USB and power management

It's easy to add and remove IP

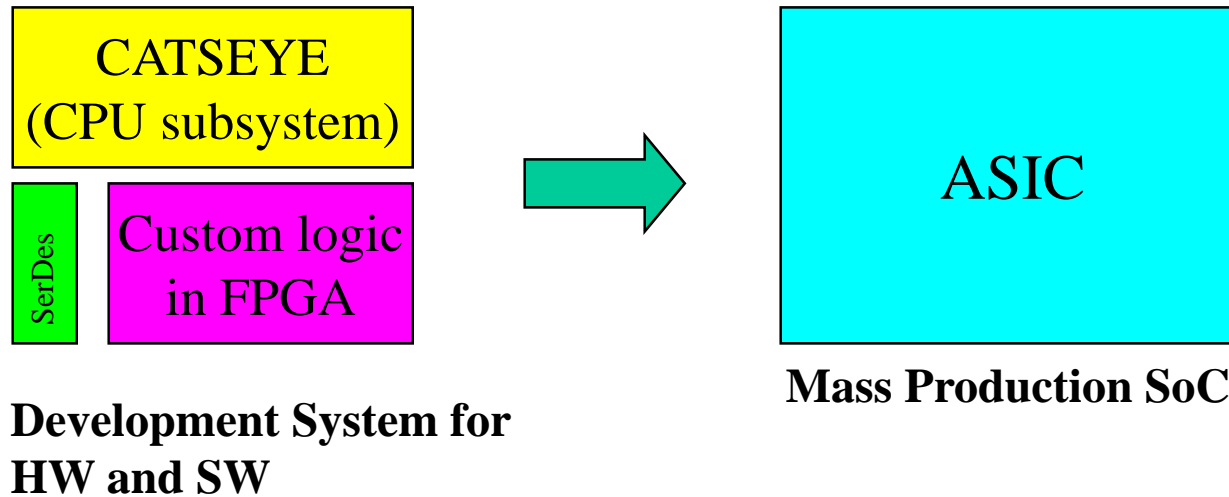
- Have a chip with just the IP needed IP
 - Drag and drop IP into SoC creator
 - Develop your own IP
 - Use K-micro developed IP
 - Use 3rd party IP
- Interface using OCP is fast and easy
 - All K-micro IP has OCP interfaces to simplify the development
 - Simple tools to make the OCP interface easy to add to IP

Change it to what you want it to be



The Verification Point

The Ending Point



Summary

- CatsEye platform development gets you to market faster with lower risk
- No need to wait for the chip to come out to begin software development
- A wide variety of qualified IP to choose from
- Easy to add your own IP to the chip

Contact Information

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