



OCP Adoption

OCP and the Brazil IP Project

The OCP standard interface is a vital component of the Brazil IP Project. The primary goal of the project is to form a consortium of universities to further human resources for designing intellectual property (IP) cores and enable System-on-Chip (SOC) design by both academia and industry in Brazil. The project has a specific goal of building a wireless applications development platform called Fenix. Fenix will be prototyped with FPGAs and will later become a SOC platform. Each IP inside Fenix will be implemented at one university participating in the Brazil IP project, and the whole will then be integrated. We are using OCP to develop the individual IP cores in order to facilitate integration.

The team working at Pontifícia Universidade Católica do Rio Grande do Sul (PUCRS), one of the universities of the Brazil IP Project, is now using OCP-IP's CoreCreator® tool to validate simple wrappers for basic cores, such as memories and small processors. The next step is to model a packet-switched NoC, with a mesh topology, using OCP at the node interfaces. The Socket Transaction Language (STL) will be used to send packets through these nodes in order to certify the correctness of the interfaces.

Membership in OCP-IP was promoted through the Hardware Design Support Research Group (GAPH), located at the Computer Science Graduate Program of the Informatics Faculty. The GAPH group, together with seven other Brazilian universities, is working on the Brazil IP project, a collaborative effort of Brazilian academic institutions to create a distributed network of integrated circuit (IC) design centers capable of delivering OCP compliant IP cores and a system-level design SOC methodology based on these cores.

For more information about PUCRS and the Brazil IP Project, visit www.pucrs.br.

OCP-IP Association, Inc.
5440 SW Westgate Drive, Suite 217, Portland, Oregon 97221 USA
Tel: 1-503-291-2560 | Fax: 1-503-297-1090 | E-mail: admin@ocpip.org
www.ocpip.org