



Yogitech Claims First OCP Universal Verification Component

SAN FRANCISCO, CA —September, 29, 2006 — Design and verification technology provider Yogitech SPA is rolling out what it claims is the industry's first mixed-language Open Core Protocol (OCP) universal verification component (UVC).

Yogitech (Pisa, Italy) said its OCP UVC is the only commercial verification solution to fully support revision 2.1 of the OCP protocol and comply with the OCP 2.0/2.1 compliant checks document, including functional coverage guidelines, released by the Open Core Protocol International Partnership earlier this year.

The component supports e and SystemVerilog verification languages, the company said.

The OCP UVC is proven by means of an advanced qualification process addressing specific customers' profiles and with the same level of coverage both for e and SystemVerilog, allowing the final user to switch between the two languages at any time, Yogitech said.

The company claims OCP UVC is the only functional verification solution that can be adopted into any verification environment based on the standard languages endorsed by the IEEE, including SystemVerilog and e, and SystemC, Verilog and VHDL design languages.

"The combination of the Yogitech OCP UVC and the Incisive verification platform [from Cadence Design Systems Inc.] is a golden solution to allow multi-language verification environments while maintaining the high level of verification productivity, predictability, and quality already achieved in the broadly deployed OCP eVC," said Silvano Motto, Yogitech CEO.

About OCP-IP

The OCP International Partnership Association, Inc. (OCP-IP), formed in 2001, promotes and supports the Open Core Protocol (OCP) as the complete socket standard ensuring rapid creation and integration of interoperable virtual components. OCP-IP's Governing Steering Committee participants include: Nokia [NYSE: NOK], Texas Instruments [NYSE: TXN], Toshiba Semiconductor Group (including Toshiba America TAEC), and Sonics. OCP-IP is a non-profit corporation delivering the first fully supported, openly licensed, core-centric protocol comprehensively fulfilling system-level integration requirements. The OCP facilitates IP core reusability and reduces design time, risk, and manufacturing costs for SoC designs. VSIA endorses the OCP socket, and OCP-IP is affiliated with the VSI Alliance. For additional background and membership information, visit www.OCPip.org.

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