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Arrow Devices, Design Rivers, EDA Express, Samplify, Silicon Integrated Systems, TrueChip, and University of Utah Join OCP-IP

BEAVERTON, OR — March 29, 2011 - OCP-IP announces seven new members who have joined the organization, including: Arrow Devices, Design Rivers, EDA Express, Samplify, Silicon Integrated Systems, TrueChip, and University of Utah. OCP-IP members receive free training and support, software tools, and documentation, enabling them to focus on the challenges of SoC design. Leveraging OCP-IP's infrastructure eliminates the need to internally design, document, train and evolve a proprietary standard and accompanying support tools, freeing up critical resources for the real design work, while providing enormous cost savings.

Arrow Devices is an IP provider focused on high-speed interfaces such as USB 3.0.

Arrow Devices' USB 3.0 Device Controller is a full feature, FPGA proven, USB 3.0 spec compliant IP core. The company also provides design/verification services based on IP development expertise in the USB domain.

Design Rivers is a design services and solutions company focused on the chip implementation of Systems-On-Chip. IP integration, development, verification and physical implementation services are offered. The Company supports flow implementation & methodology design as well as all aspects of power, size and performance analysis, optimization and implementation.

EDA Express administers an informational site of EDA tools for Japanese engineers.

Samplify is a solution provider of technology, semiconductor and system products for the medical imaging, wireless infrastructure, and industrial markets. Headquartered in Santa Clara, CA, Samplify's technology and semiconductor products, including the SAM1600 family of compressing ADCs and its AutoFocus™ ultrasound beamforming technology, have won multiple industry awards for innovation.

Silicon Integrated Systems (SiS) focuses on cutting-edge SoC solutions to meet various applications for digital era, including: touch and tablet devices, HDTV, internet TV, portable CE, personal computers and customized SoC design service. SiS is headquartered in Taiwan and listed on the Taiwan Stock Exchange under the code "2363"

Truechip provides IP Solutions for interface buses and bridges across serial/parallel interfaces, connectivity IPs and video & imaging solutions.

University of Utah College of Engineering prepares students for leadership positions and professional practice in academia, industry and government to improve the productivity, health, safety and enjoyment of human life through leading-edge research and to stimulate and grow the economy by providing qualified engineering professionals.

“These seven new members illustrate the importance of a standard, open interface for IP,” said Ian Mackintosh, president OCP-IP. “OCP-IP not only provides the OCP specification, but also a complete infrastructure that allows our members to quickly and easily implement OCP into world-class, leading-edge designs.”

To view some of the many application areas where OCP is used, please see our “OCP Inside” presentation available at: http://www.ocpip.org/ocp_inside.php

For the latest information on OCP-IP please see our newsletter at <http://www.ocpip.org/newsletters.php>

About OCP-IP

Formed in 2001, OCP-IP is a non-profit corporation promoting, supporting and delivering the only openly licensed, **core-centric protocol** comprehensively fulfilling integration requirements of **heterogeneous multicore** systems. The Open Core Protocol (OCP) facilitates IP **core reusability** and reduces design time, risk, and manufacturing costs for all SoC and **electronic designs** by providing a comprehensive supporting infrastructure. For additional background and membership information, visit www.OCP-IP.org.

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