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## **OCP-IP Announces Online DATE Virtual-Conference Presentations**

**BEAVERTON, OR** – March 14, 2011 – Open Core Protocol International Partnership (OCP-IP) has again posted their annual online “Presentations from DATE”. The Design Automation and Test Europe (DATE) Conference is held 14-18 March 2011 in Grenoble, France. The presentations offer a fresh opportunity to see the latest products, tools, and services utilizing OCP technology.

Presentations are freely available and discuss the diverse issues, challenges, and topics surrounding re-usable IP, including: IP development, purchasing, interconnect, verification and testing, IP quality, network on chip (NOC) from OCP-IP member companies: Alpha Data, Carbon Design Systems, Cadence Design Systems, CAST, Duolog Technologies, Jasper Design Automation, MIPS Technologies, OCP-IP’s NoC Benchmarking Working Group, Sonics, Synopsys, and more.

“OCP-IP is dedicated to proliferating a common standard for intellectual property core interfaces that facilitate ‘plug and play’ SoC design,” said Ian Mackintosh, president and chairman of OCP-IP. “Both our DATE presentations and our [OCP-Inside presentation](#) offer fresh opportunities to see the exceptional ways our members are leveraging both OCP and the infrastructure surrounding the OCP specification.”

To view the OCP-Inside presentation, see:

[http://www.ocpip.org/ocp\\_inside.php](http://www.ocpip.org/ocp_inside.php)

To review the presentations, please see:

[http://www.ocpip.org/papers\\_and\\_presentations.php](http://www.ocpip.org/papers_and_presentations.php)

For all the latest information on OCP-IP please see our latest newsletter at:

<http://www.ocpip.org/newsletters.php>

### **About OCP-IP**

Formed in 2001, OCP-IP is a non-profit corporation promoting, supporting and delivering the only openly licensed, **core-centric protocol** comprehensively fulfilling integration requirements of **heterogeneous multicore** systems. The Open Core Protocol (OCP) facilitates IP **core reusability** and reduces design time, risk, and manufacturing costs for all SoC and **electronic designs** by providing a comprehensive supporting infrastructure. For additional background and membership information, visit [www.OCP-IP.org](http://www.OCP-IP.org).

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