

The Complete OCP Compliance Environment

CoreCreator[®] provides a single graphical or command line based environment for validating Open Core Protocol (OCP) implementations and “componentizing” OCP compatible cores. CoreCreator streamlines the generation and packaging of core models, interfaces, timing parameters, synthesis scripts, test vectors, and verification suites necessary for efficient IP core reuse and SoC integration.

Validation and componentization is accomplished through three systematized tasks: (1) validate the OCP implementation, (2) capture the relevant core representations, and (3) package the core for reuse.

CoreCreator also provides an environment for stimulating a core (or multiple cores) and analyzing performance and functionality in a system environment. CoreCreator’s high degree of automation, featuring tight feedback loops, enables cores to be rapidly componentized to make reuse straightforward and predictable.

Key Features

Comprehensive Design Environment for Importing Existing IP Cores or Creating New IP Cores

- OCP protocol and physical constraint compliance verification
- Maximum frequency and gate area estimation

Automated Functional Verification Environment

- Highly automated environment for configuration, simulation, logic synthesis and timing analysis
- IP core traffic trace generation
- Configurable OCP behavioral models for core validation independent of a full system implementation
- OCP behavioral models can be controlled using Verilog Task Interface
- User-defined (non-OCP) core interface support
- TCL-scripting for auto-generation of configuration files and timing constraints

IP Core Packaging for Plug-and-Play Integration

- Physical constraint extractor and packager
- Packager for all necessary verification files including scan test and functional vectors

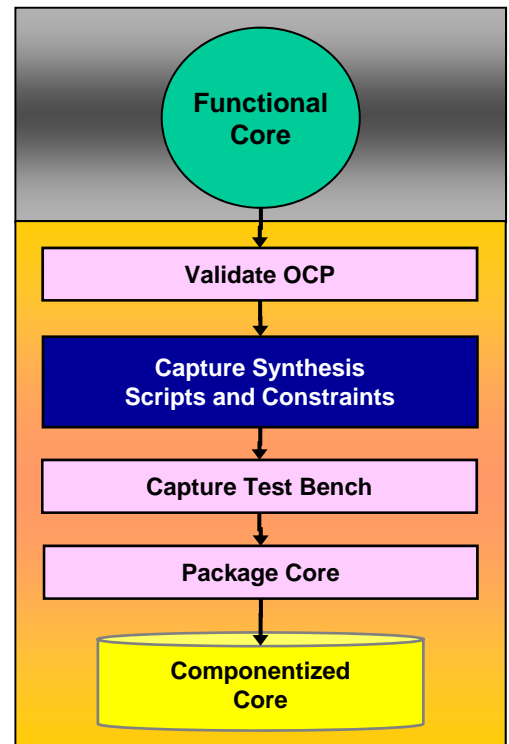
Benefits

Rapid Time to Market

- Streamlines and automates OCP validation
- Decouples core and system development
- Enables multiple cores to be developed in parallel
- Provides a structured core debug and verification environment allowing system-level design verification

Complete IP Reuse

- Standardizes core I/O
- Builds portable test bench



Streamlined OCP validation and componentization

The complete Open Core Protocol Specification is available at
www.ocpip.org

